

CLAIMS

What is claimed is:

- Sub A37
1. A signaling apparatus, comprising:
 data input means for receiving user input;
 5 a memory having signaling data stored therein;
 a processor, coupled to the memory and the data input means, for generating a
 signal sequence in response to a user input, the signal sequence comprising a 5 pulse
 position modulated (5PPM) signal;
 a modulator, coupled to the processor, for modulating the signal sequence onto a
 10 carrier signal;
 a transmitter, coupled to the modulator, for transmitting the modulated signal
 sequence, including the 5 pulse position modulated signal.
 2. The apparatus of claim 1, wherein a last position of said 5PPM signal is
 15 always set to a low bit.
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 3. The apparatus of claim 1, wherein said signal sequence is defined by a
 signaling protocol having a header portion and a payload portion.
 4. The apparatus of claim 3, wherein said header portion comprises a plurality
 20 of fields for defining said payload portion.
 5. The apparatus of claim 3, wherein said payload portion is a variable bit
 stream.
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 6. The apparatus of claim 1, wherein said data input means comprises a
 keyboard.
 7. The apparatus of claim 1, wherein said signal sequence defines a position on
 30 a display of an image display device.
 8. In a control system having a signal transmitter for encoding and transmitting
 control signals and a signal receiver for receiving and decoding the control signals, the

receiver comprising means for decoding a control signal in response to rising edges of pulses in the control signal, a control signal structure comprising:

a payload portion; and

a header portion, said header portion comprising a plurality of fields for defining

5 said payload portion,

said header and payload portions being represented as a plurality of symbols encoded in accordance with a 5 pulse position modulation (5PPM) scheme, wherein each of said encoded symbols includes a last position that is always set to a low bit.

10 9. The data structure of claim 8, wherein said payload portion indicates a keyboard character.

10. The data structure of claim 8, wherein said payload portion indicates coordinates for a pointing device.

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11. The apparatus of claim 8, wherein said header portion comprises a repeat field for selectively transmitting only said header portion.

12. In a control system having a signal transmitter for encoding and transmitting
20 control signals and a signal receiver for receiving and decoding the control signals, the receiver comprising means for decoding a control signal in response to rising edge of pulses in the control signal, a method of providing control signals, comprising:

generating a signal sequence in response to a user input; and

converting said signal sequence into a plurality of symbols in accordance with a

25 5 pulse position modulated (5PPM) scheme,

wherein said converting step comprises setting a last position of each said 5 PPM symbol to a low bit.

13. The method of claim 12, further comprising the step of defining said symbol
30 sequence by a signaling protocol having a header portion and a payload portion.

14. The method of claim 12, further comprising the step of sending bits representing a keyboard character as said payload portion.

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15. The method of claim 12, further comprising the step of sending bits representing a coordinate for a pointing device as said payload portion.

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